Attorney's Docket No.: 12732-103001 / US5749

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shou Nagao et al.

Art Unit : Unknown Examiner: Unknown

Serial No.: New Application

: May 15, 2002

Filed Title

: PULSE OUTPUT CIRCUIT, SHIFT REGISTER, AND DISPLAY DEVICE

Commissioner for Patents Washington, D.C. 20231

TRANSMITTAL OF PRIORITY DOCUMENT UNDER 35 USC §119

Applicants hereby confirm their claim of priority under 35 USC §119 from the following application:

Japan Application No. 2001-160140 filed May 29, 2001

A certified copy of the application from which priority is claimed is submitted herewith. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: May 15, 2002

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日本国特許庁 JAPAN PATENT OFFICE

別紙添付の書類に記載されている事項は下記の出願書類に記載されている事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed with this Office

出願年月日

Date of Application:

2001年 5月29日

出願番号

Application Number:

人

特願2001-160140

ST.10/C]:

[JP2001-160140]

力 願 pplicant(s):

株式会社半導体エネルギー研究所

2002年 4月12日





